W W

Fig. 1

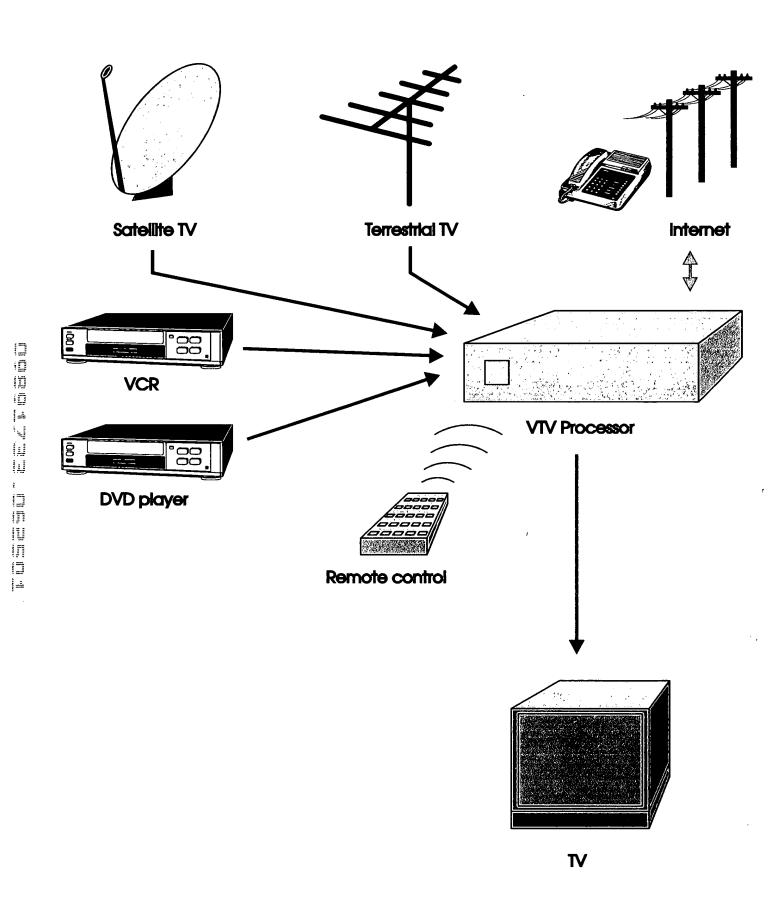


Fig. 2



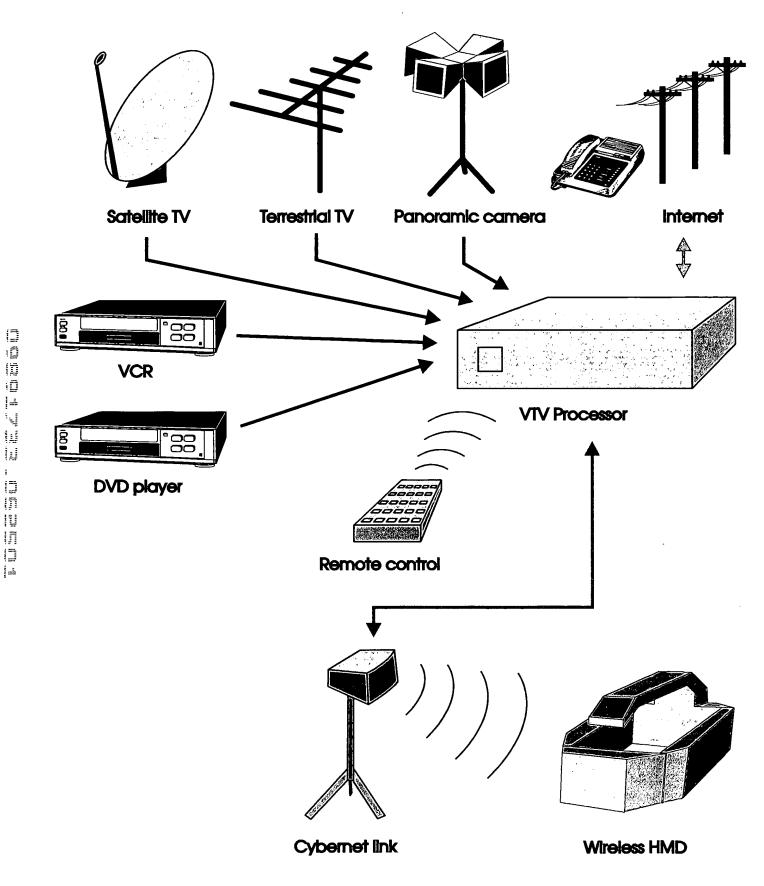


Fig. 3

Display fie

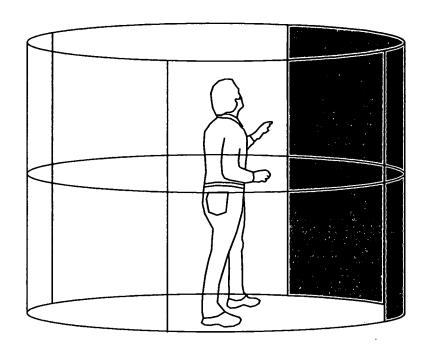


Fig. 4

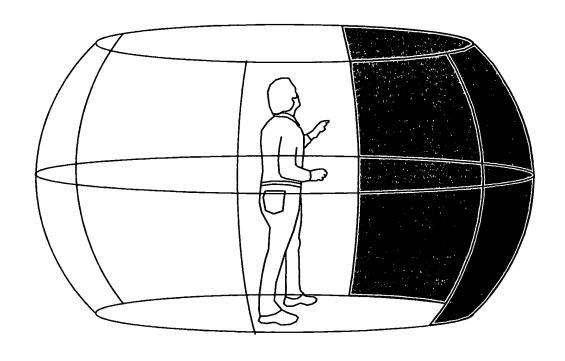
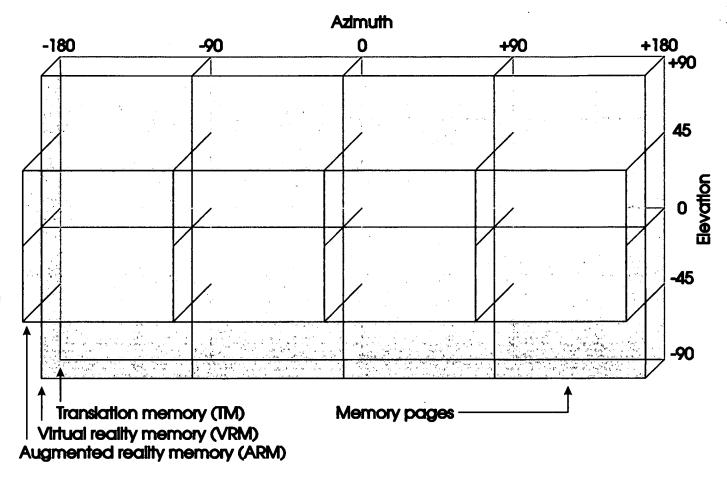


Fig. 5

Fig. 7





VTV graphies engine: (data write side)

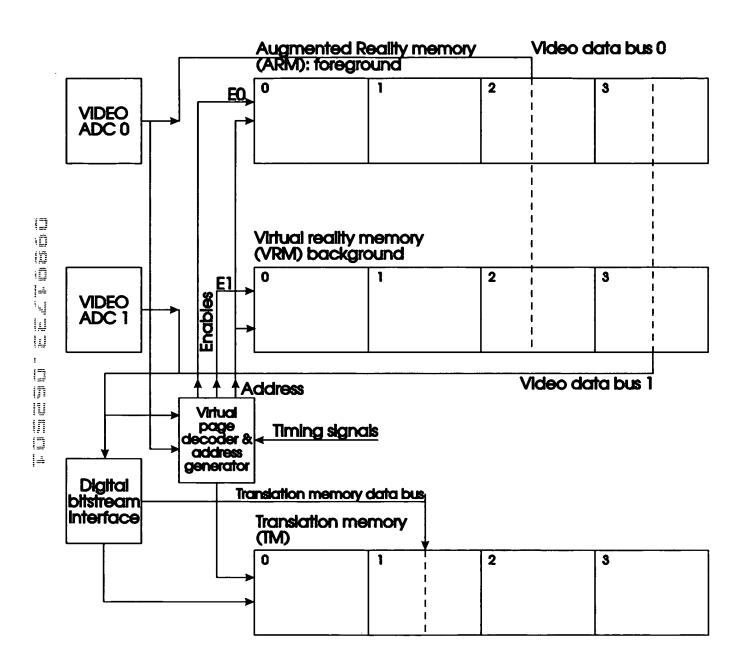


Fig. 9

VTV graphies engine (data read side)

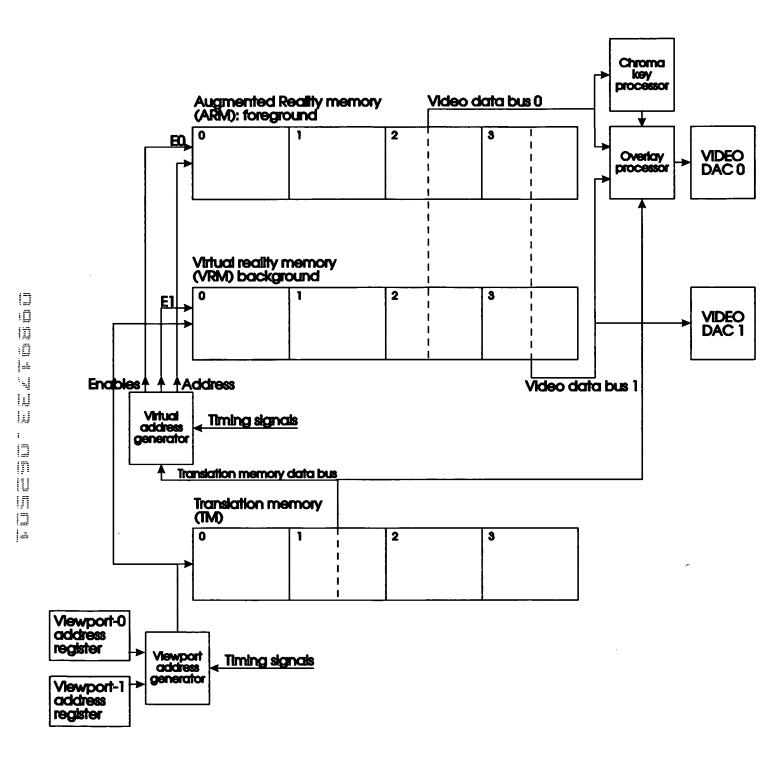


Fig. 10

Analogue deo compatibility:

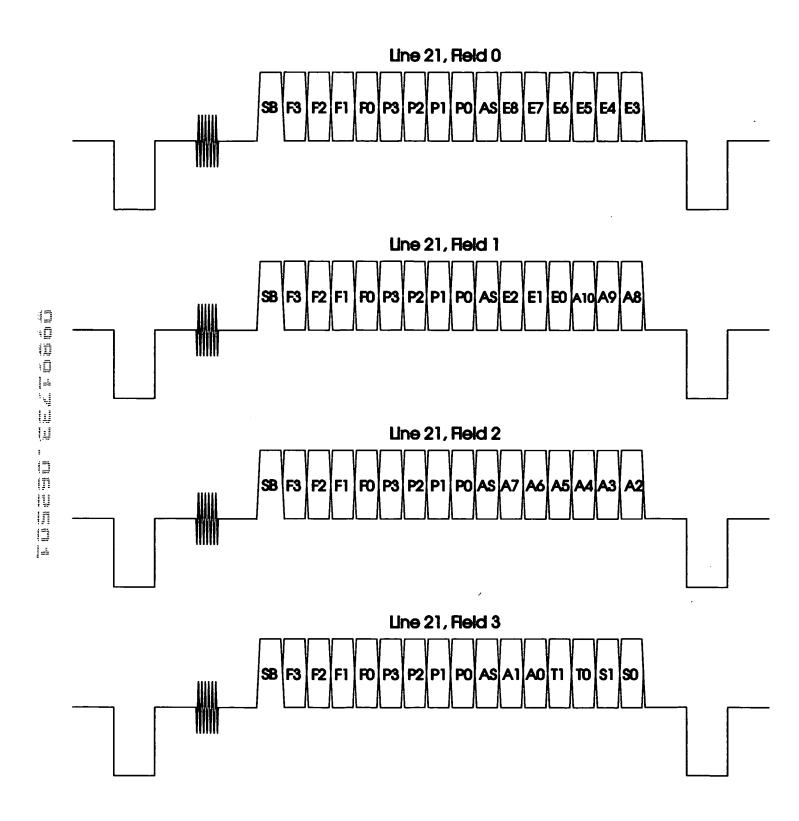


Fig. 11

CONTROL FIELD	BITS	VALUE	ASSIGNMENT KEY
FRAME FLIP	FF		FLIP MEMORY FRAMES
FIELD TYPE	F2-F0		FOREGROUND VIDEO (ARM)
	ļ		BACKGROUND VIDEO (VRM)
	 		DIGITAL HYBRID (TM)
	 		DIGITAL BIT STREAM FRAME (TM) RESERVED
	 		RESERVED
	 		RESERVED
		<u> </u>	DIGITAL CONTROL FRAME
	 	······································	DIGITAL CONTROL FRANCE
PAGE NUMBER	P3-P0	0-15	(DEPENDANT UPON MEM LAYOUT)
AUDIO SYNC	AS		RESET AUDIO BUFFER TO ZERO
ELEVATION CORRECTION	E8-E0	(+/- 45 DEG)	CAMERA ELEVATION
AZIMUTH CORRECTION	A10-A0	(+/- 180 DEG)	CAMERA AZIMUTH
AUDIO TRACKS	T1-T0		NO AUDIO TRACKS
	ļ		4 AUDIO TRACKS
	ļ		8 AUDIO TRACKS
	 	3	OBJECT BASED AUDIO
AUDIO SAMPLE RATE	S1-S0	0	2/4 LINES (15K S/S)
			3/6 LINES (23K S/S)
			4/8 LINES (31k S/S)
			5/10 LINES (38K S/S)

TABLE 1

Analogue de compatibilis: (8 channel, low sample rate example)

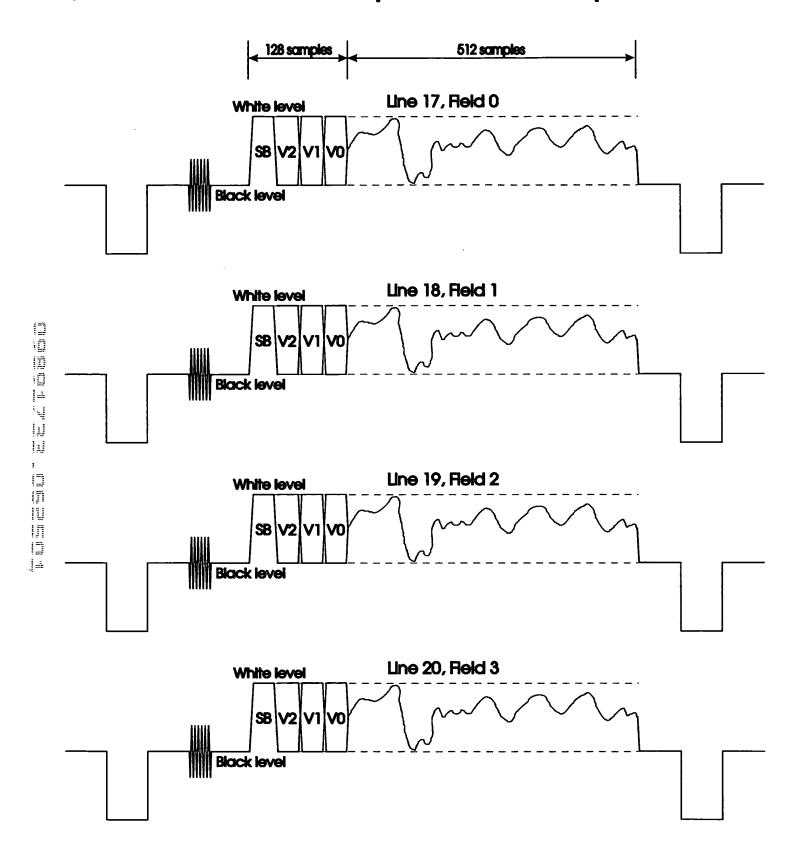
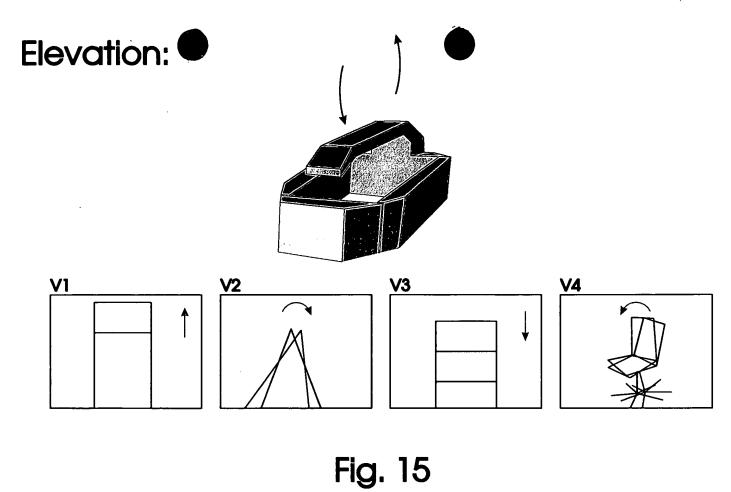


Fig. 12

Fig. 14



Roll:

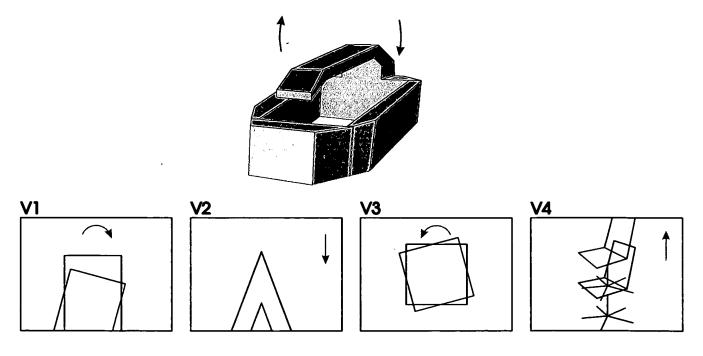
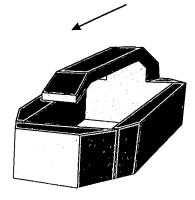
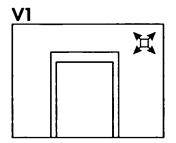
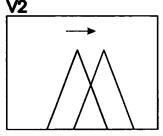
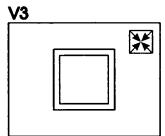


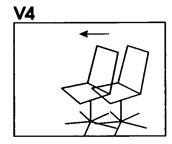
Fig. 16











×

Fig. 17

Left/Right:

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P. L.J. 2011 graft fresh fresh fresh

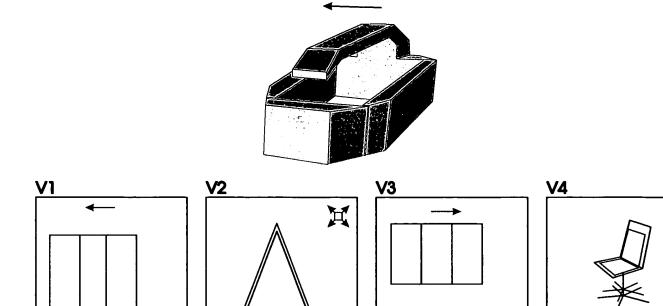
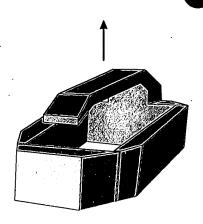


Fig. 18



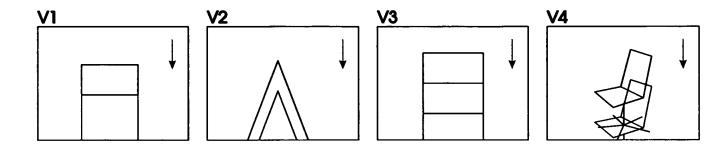


Fig. 19

Optical tracking hardware: (simplified system)

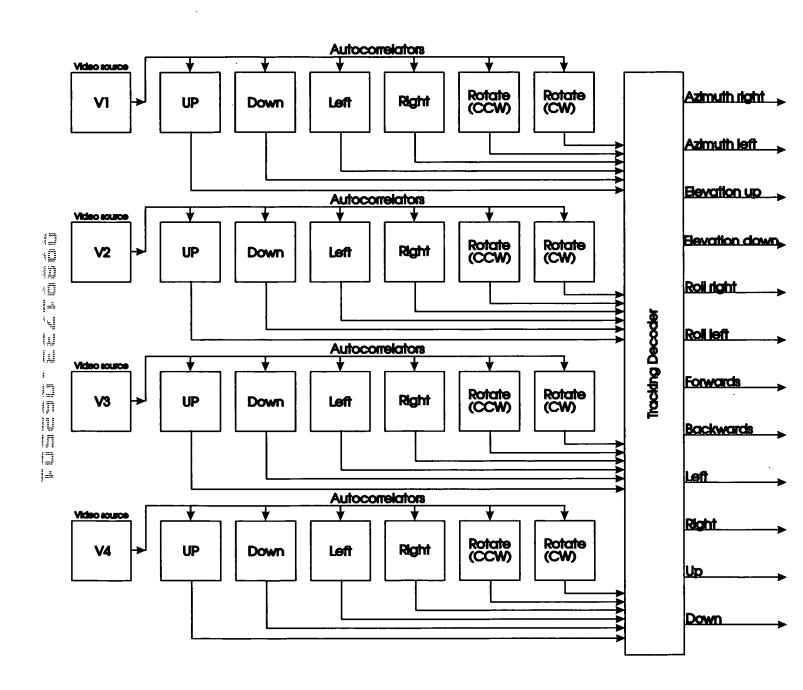


Fig. 20